

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James F. Buller

Serial No.: 10/676,904

Filed: October 1, 2003

For: LATERAL DIODE WITH
MULTIPLE SPACERS

§ Group No.: 2823

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§ Examiner: Unknown

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§ Atty. Dkt. No.: AMDI:132/HON

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Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

CERTIFICATE OF MAILING 37 C.F.R. 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below:

January 2, 2004

Date

Timothy M. Honeycutt

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. 1.97(b)(3) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. 1.56(a), Applicant respectfully requests that this Information Disclosure Statement be entered and that the reference(s) listed in the form PTO-1449 filed concurrently herewith be considered by the Examiner and made of record. Copies of any non-U.S. patent references listed in the accompanying form PTO-1449 are enclosed for the convenience of the Examiner.

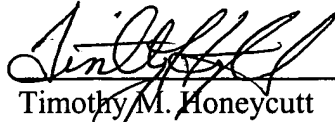
In accordance with 37 C.F.R. 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information cited in the accompanying form PTO-1449 is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. 1.56(b), exists.

The information is listed in the accompanying form PTO-1449 in accordance with 37 C.F.R. 1.98. Any explanation of non-English language documents cited in the accompanying form PTO-1449 is believed to constitute a concise explanation of the relevance of the listed reference(s) as they are presently understood by the individual designated in 37 C.F.R. 1.56(c) most knowledgeable about the content of the listed reference(s), in accordance with 37 C.F.R. 1.98(a)(3).

The Commissioner is authorized to charge any required fees or credit any overpayment to
Deposit Account No. 01-0365, Order No. AMDI:132HON.

Respectfully submitted,

Date: January 2, 2004



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Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DOCKET NO. AMDI:132\HON	SERIAL NO. 10/676,904
	APPLICANT James F. Buller et al.	CERTIFICATE OF MAILING 37 C.F.R. 1.8 I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below: <i>January 2, 2004</i> <i>Timothy M. Honeycutt</i> Date Timothy M. Honeycutt
	FILING DATE October 1, 2003	GROUP 2823

U.S. PATENT DOCUMENTS

EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A1	6,074,906	June 13, 2000	Jon Cheek et al.	438	231	
	A2	US 6,506,642 B1	January 14, 2003	Scott Luning et al.	438	231	

FOREIGN PATENT DOCUMENTS

EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION

OTHER ART

EXAM INIT.	REF. DES.	AUTHOR; TITLE; JOURNAL; VOLUME; DATE; PERTINENT PAGES
	A3	Dallas Semiconductor; <i>Compensating for Ideality Factor and Series Resistance Differences between Thermal Sense Diodes</i> ; www.maxim-ic.com/an1057 ; April 17, 2002; pp. 1-2
	A4	Intel; <i>Intel Pentium® III Processor, Thermal Diode</i> ; http://support.intel.com/support/processors/pentiumiii/sb/CS-007590.htm ; August 12, 2003; pp. 1-3

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement--PTO-1449 (Modified)